

**IN THE SPECIFICATION**

Please amend the paragraph beginning at page 7, line 2 as follows:

Figure 2 shows a photodetection arrangement in accordance with an embodiment of the present invention. An APD detector 10 in an integrated package 12 is fed from a power amplifier 40, the output of which is determined by a digital to analog converter 50 which is set to provide the required supply for the particular APD in the use.

Please amend the paragraph beginning at page 7, line 10 as follows:

The output of the amplifier is connected to a comparator 22, the other input of which is fed by a reference voltage 23. The comparator output feeds the clock input of a D-type flip-flop 24, which will trigger when the output of the instrumentation amplifier exceeds the reference voltage supplied to the comparator. The output of the flip-flop controls an analogue switch 25 which pulls down the input to the power amplifier 40 supplying the detector 10. When this switch is closed, the supply to the detector will be removed, effectively switching it off and thereby rendering it insensitive to any input at fiberoptic 16 and in particular to any overload input level that would have caused damage to the device 10 had it remained biased into operation by the power amplifier 14.

**IN THE TITLE**

**Please cancel the original title and substitute with the new title as follows:**

**A PHOTODETECTION CIRCUIT**